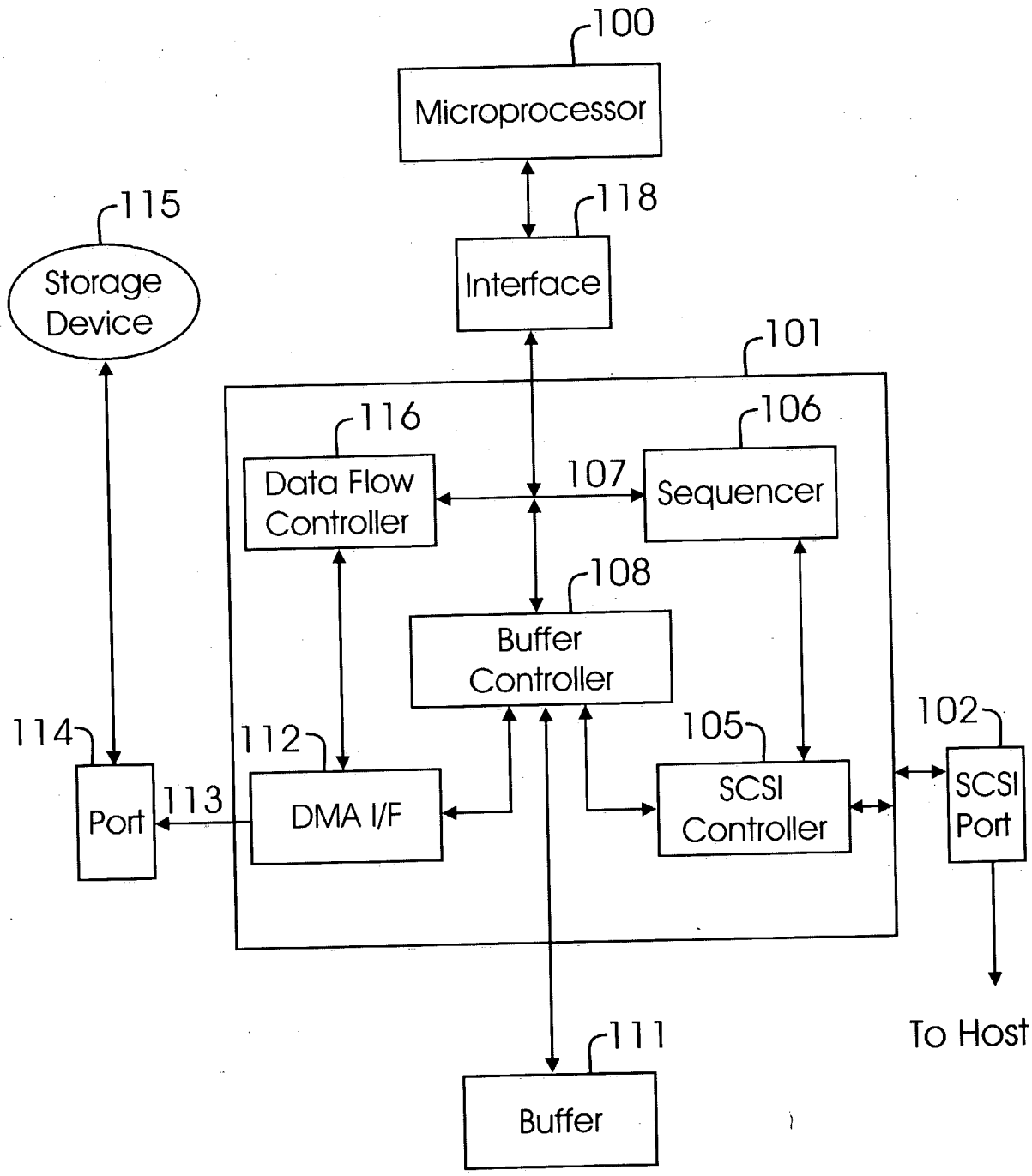
100
Microprocessor
118
Interface
115
Storage Device
101
116
Data Flow Controller
107
106
Sequencer
108
Buffer Controller
114
112
105
102
Port
113
DMA I/F
SCSI Controller
SCSI Port
To Host
111
Buffer

FIGURE 1A

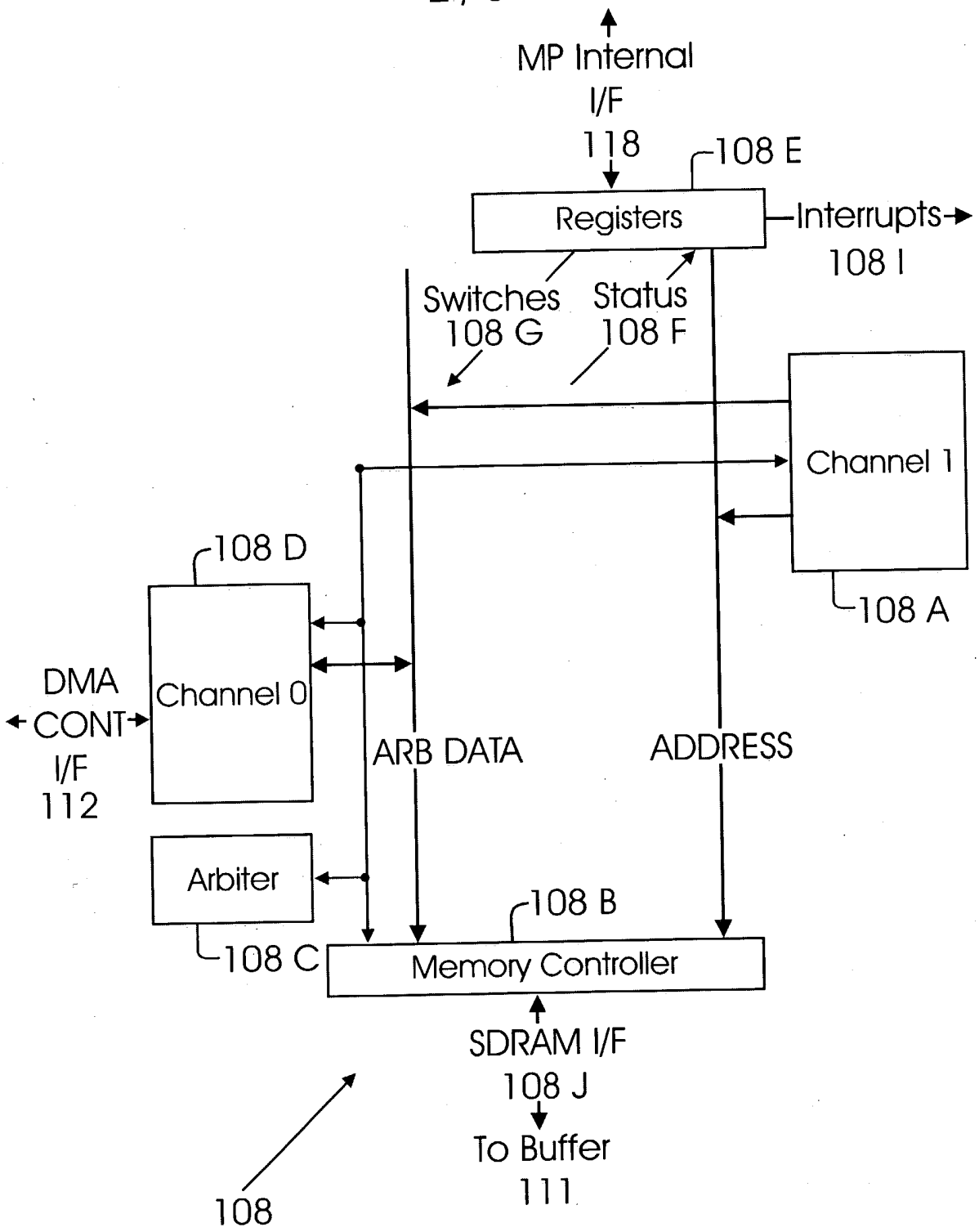
MP Internal
I/F
118
108 E
Registers
Interrupts
108 I
Switches
108 G
Status
108 F
Channel 1
108 A
108 D
Channel 0
DMA
CONT
I/F
112
ARB DATA
ADDRESS
Arbiter
108 B
108 C
Memory Controller
SDRAM I/F
108 J
To Buffer
111
108

FIGURE 1B

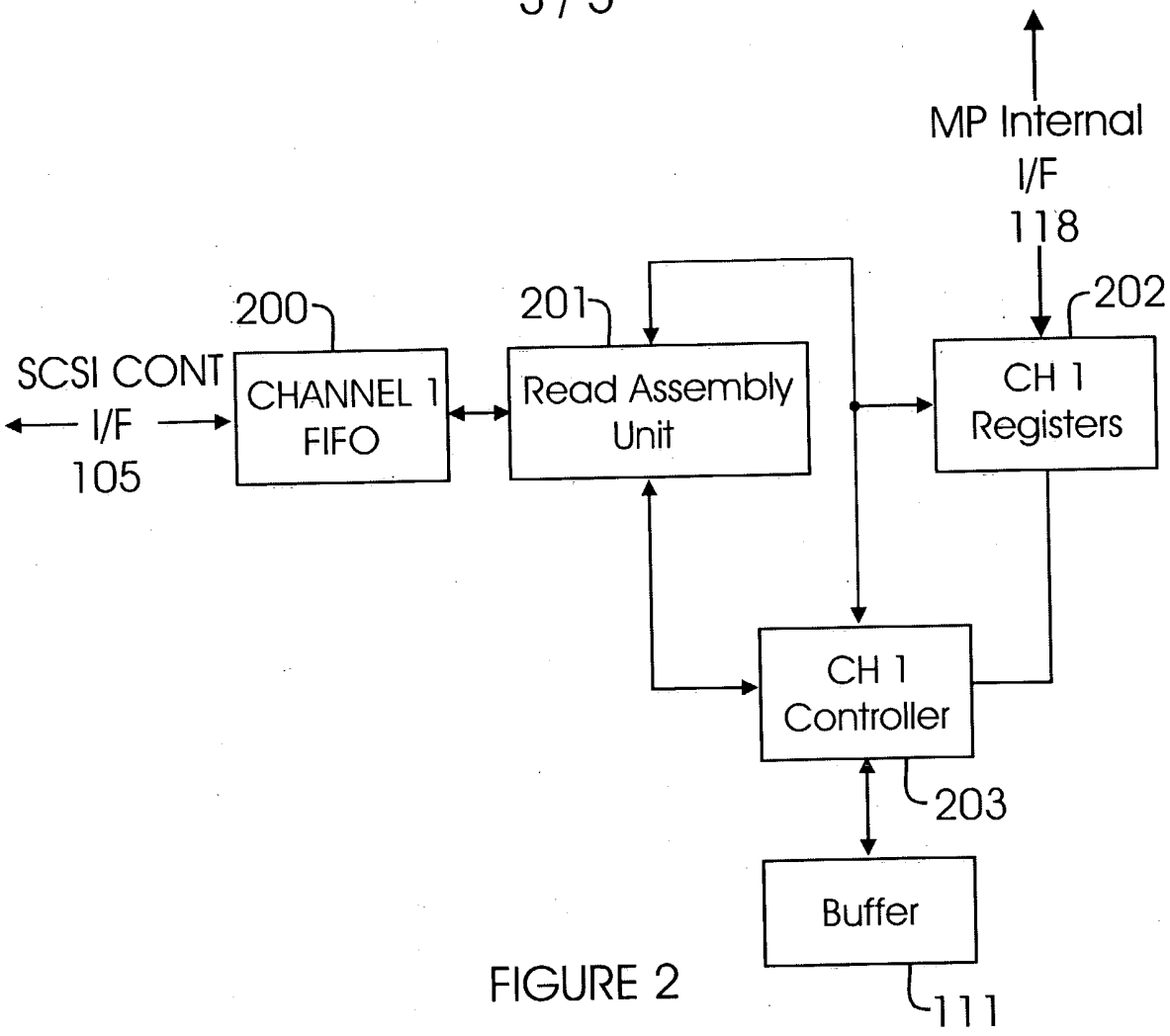

FIGURE 2

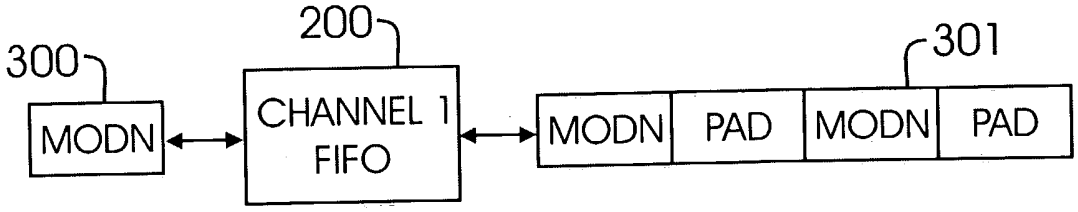

FIGURE 3

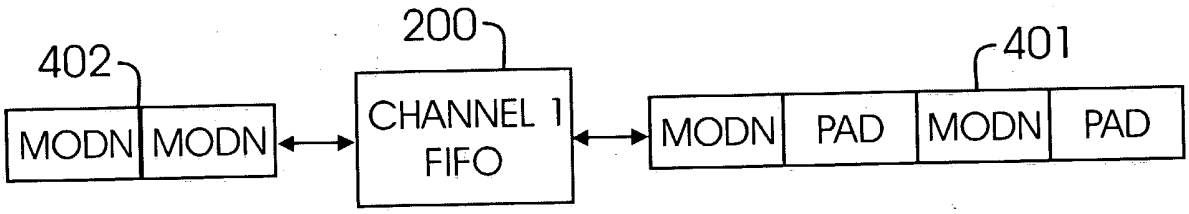

FIGURE 4

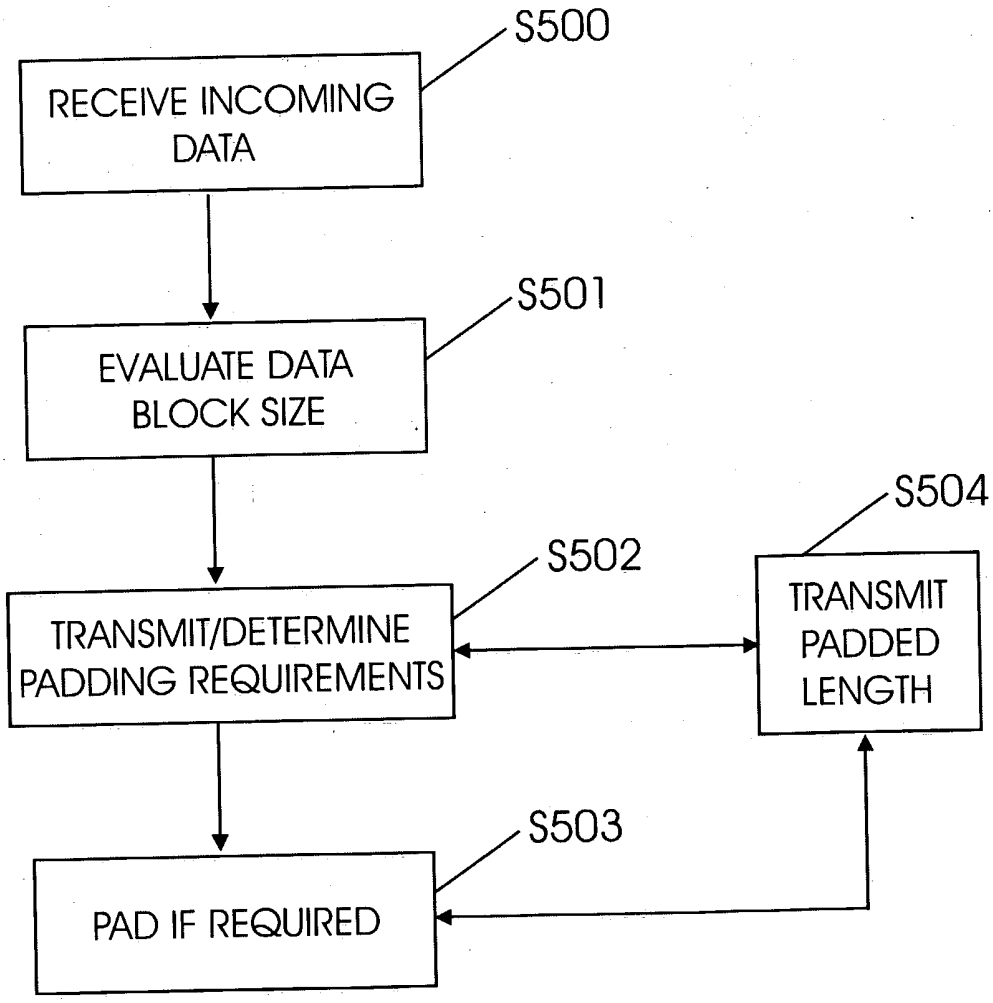

FIGURE 5

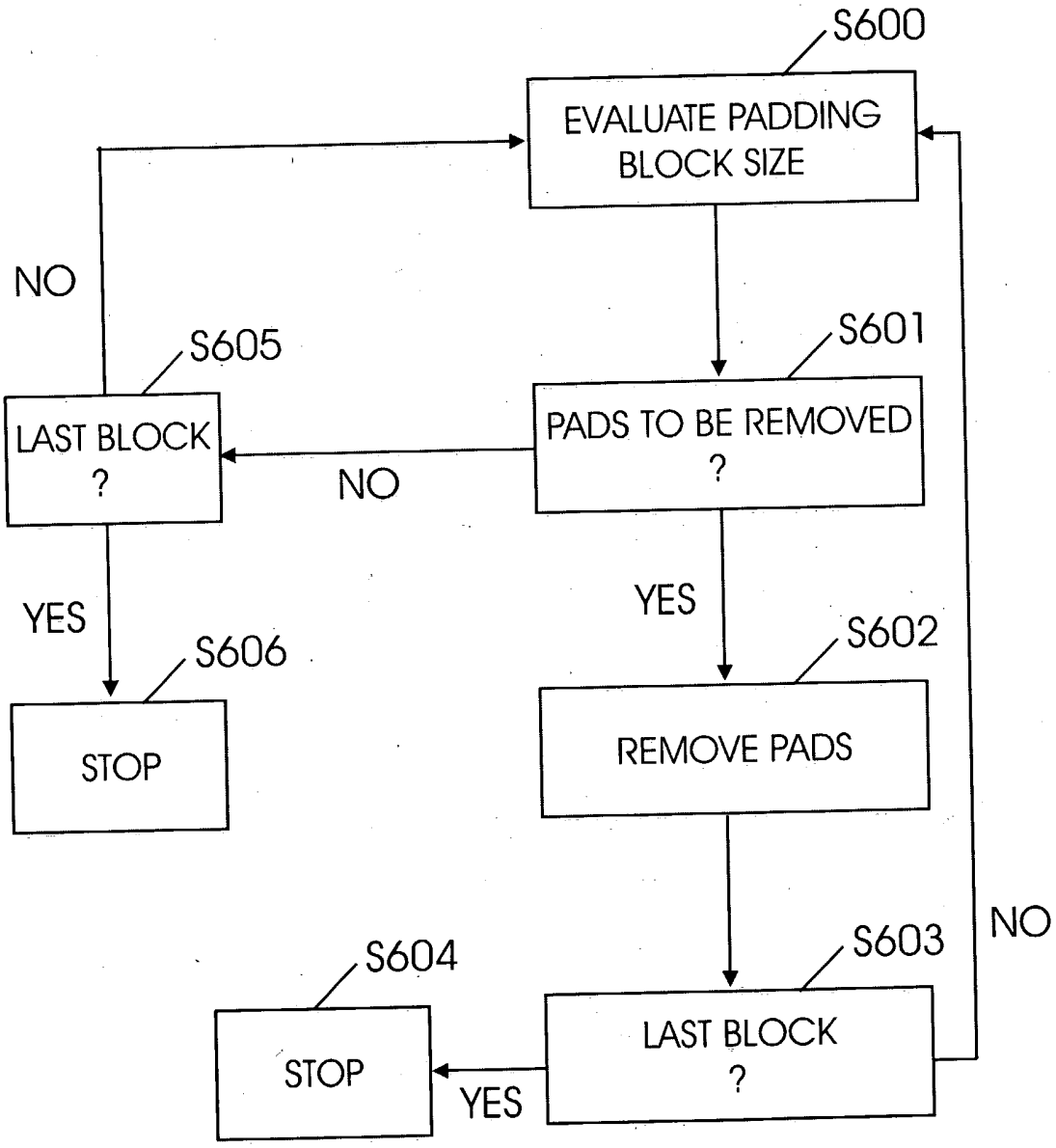
S600
EVALUATE PADDING BLOCK SIZE
NO
S605
LAST BLOCK ?
S601
PADS TO BE REMOVED ?
NO
YES
S606
STOP
YES
S602
REMOVE PADS
NO
S604
STOP
YES
S603
LAST BLOCK ?

FIGURE 6